ROBUST PERFORMANCE THAT LEADS AND EXCEEDS

VeriFone's VX 520 leads the way with unprecedented performance, mission-critical features and more of everything you want in a countertop payment device. More revenue opportunities. Flexible connectivity. Uncompromising security and

dependability. All on the proven VX platform.



Processor

400 MHz ARM11 32-bit RISC processor

160 MB (128 MB of Flash, 32 MB of SDRAM) standard, designed to support up to 500 MB

128x64 pixel graphical (white backlit) LCD with backlighting; supports up to 8 lines x 21 characters





Standard

PROVEN, POPULAR PLATFORM - EVOLVED FOR TODAY

- Rely on it the VX platform together with the time-tested Verix operating system, for the world's best-selling payment devices
- Consistency and high usability ensure lower cost of ownership and ease of use

UNPARALLELED FEATURES DELIVER NEXT-LEVEL PERFORMANCE

- Available with VeriFone's NFC software technology, enabling full flexibility to manage multiple NFC mobile wallets, apps and programs
- Generates new recurring revenues from value-added applications, thanks to an extraordinary increase in memory of 160 MB standard, designed to support up to 500 MB
- Takes advantage of the industry's fastest processor to handle encryption, decryption and processing at lightning speeds - moving more transactions in the same time for greater profits
- · Offers a full range of connectivity options with choice of dial or dial and Ethernet
- Uniquely designed communication port area neatly connects cables under the device for clean countertops
- Combines an ergonomic design, small footprint and unique cable management system to optimize handover usage

REASSURING CONFIDENCE FROM FULL-SPECTRUM SECURITY

- EMV-ready with standard PCI PTS 3.X
- Provides end-to-end encryption with VeriShield Total Protect, secured by RSA, to maximize protection against fraud and misuse

SUPERIOR VALUE FROM EXTENDED-LIFE DESIGN

- Leverages VeriFone's unmatched reputation for quality with longer-life components to extend the field life of devices and minimize repair frequency
- Metal dome keypad design delivers one of the most reliable keypads in the POS industry, withstanding more than one million key presses
- · Landed smart card reader is built for 5x the life expectancy of friction readers

Triple track (tracks 1, 2, 3), high coercivity, bi-directional

Primary Smart Card

ISO 7816, 1.8V, 3V, 5V; synchronous and asynchronous cards; EMV Level 1 and 2 Type approved

SAM Card Reader (optional)

3 Security Access Modules (SAMs)

Peripheral Ports

One Ethernet (10/100BaseT), one telco, one RS-232 port, one USB 2.0 Host port and one USB Client port supporting peripherals including PIN pads and contactless devices

Printer

Integrated thermal with graphics capabilities, 24 lines per second, 24 or 32 columns; standard roll paper 58mm (2.25 in.) x 25M (82 ft.), paper roll size 40mm (1.6 in.) (Contactless only), 49mm (1.9 in.) (all other variants), single ply

Standard 56 kbps modem Bell 103/212a, CCITT V.21/V.22/V.22bis/V.32/V.32bis (300/1200/2400/9600/ 14400/33.6/56 and HC FastConnect for 1200bps)

Application selects between asynchronous protocols (Visa 1, Visa 2 and others) and synchronous protocols (including ISO 8583/SDLC); Communication protocol is V.92/V.34/V.32 BIS/V.32

SSL v3.0, 3DES encryption, Master/Session and DUKPT key management | PCI PTS 3.X is standard | VeriShield file authentication

Length: 203mm (8.0in.); Width: 87mm (3.4in.); Height: 76mm (3.0in.); Weight: 500g

Environmental

0° to 40° C (32° to 104° F) operating temperature; 5% to 90% relative humidity, non-condensing; -20° to 60° C (-4° to 140° F) storage temperature

AC input 100 - 240 VAC, 50/60 Hz; DC output 8V 2.25A

Contactless Applications

MasterCard PayPass M/Chip | MasterCard PayPass MagStripe | Visa payWave MSD | Visa payWave qVSDC | Discover Zip | American Express ExpressPay | Pass-through access to ISO 14443-4 and MiFare card, for direct host terminal based application access | Secure SoftSAM | Hardware SAM optional

NFC Applications

Support for ISO 18092

